



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **NAKAMURA, Keishi et al.**Group Art Unit **2832**Serial No **09/825,446**Examiner **Karl D. Easthom**Filed **April 4, 2001**P.T.O. Confirmation No.: **1801**For **LOW RESISTANCE VALUE RESISTOR****AMENDMENT TRANSMITTAL**Commissioner for Patents
Washington, D.C. 20231

January 9, 2003

Sir:

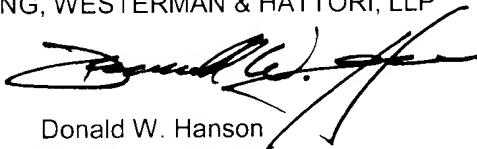
Transmitted herewith is an Amendment in the above-identified application. The fee has been calculated as shown below:

	CLAIMS AS AMENDED						
	Claims Remaining After Amendment	Highest Number Previously Paid For		Present Extra	Small Entity	Large Entity	Additional Fee
Total Claims	34	20	=	14	X \$9	X \$18	\$252.00
Independe nt Claims	3	3	=	0	X \$42	X \$84	\$0.00
## First Presentation of Multiple Dependent Claims					\$140	280	
TOTAL FEES ENCLOSED:							\$252.00

XX Enclosed please find our check in the amount of **\$252.00** for the additional claims fee in connection with this amendment. The Commissioner is hereby authorized to charge payment for any additional fees associated with this communication or credit any overpayment to Deposit Account No. 01-2340. Two duplicates of this sheet are attached.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP


Donald W. Hanson
Attorney for Applicants
Reg. No. 27,133

Dwh/nk

Atty Docket No. 010481
Suite 1000, 1725 K Street, N.W.
Washington, D.C. 20006
(202) 659-2930

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of:

NAKAMURA, Keishi et al.

Group Art Unit: **2832**

Serial No.: **09/825,446**

Examiner: **Karl D. Easthom**

Filed: **April 4, 2001**

P.T.O. Confirmation No.: **1801**

For. **LOW RESISTANCE VALUE RESISTOR**

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Date: January 9, 2003

PROCESSED
JAN 13 2003
TECHNICAL SERVICES

Sir:

In response to the Office Action dated October 9, 2002, please amend the above-identified application as set forth below:

IN THE CLAIMS:

Please cancel claims 2-4, 6-12 and 14-20 without prejudice or disclaimer

Please amend the claim 1 as follows:

1. (Thrice Amended) A low resistance value resistor comprising:

a resistor body comprised by a resistive alloy;

at least two electrodes, comprised by metal strips of flat tetragonal shape having a high electrical conductivity, each of said metal strips having a same width with a width of said resistor body, and affixed on one surface of the resistor body separately wherein a diffusion layer is formed at an interface between the resistor body and the metal strip or in an interior of the resistor body under the metal strip;